From 9034af8a07b7321c391f1ef302d76ac2214d844d Mon Sep 17 00:00:00 2001

From: mk <mohan10415krishna@gmail.com>

**Date: Sun, 9 Oct 2016 19:56:39 +0530**

**Subject: [PATCH] our patch for touchscreen**

---

arch/arm/boot/dts/am335x-bone-common.dtsi | 58 +++++++++++++++++++++++++++++++

1 file changed, 58 insertions(+)

diff --git a/arch/arm/boot/dts/am335x-bone-common.dtsi b/arch/arm/boot/dts/am335x-bone-common.dtsi

index 5270d18..d872b8a 100644

--- a/arch/arm/boot/dts/am335x-bone-common.dtsi

+++ b/arch/arm/boot/dts/am335x-bone-common.dtsi

@@ -84,6 +84,27 @@

>;

};

+

+/\*own i2c1 enabled ========> \*/

+

+ i2c1\_pins: pinmux\_i2c1\_pins {

+ pinctrl-single,pins = <

+ 0x158 0x72 /\*(PIN\_INPUT\_PULLUP | MUX\_MODE2) / i2c1\_sda.i2c1\_sda/ \*/

+ 0x15c 0x72 /\*(PIN\_INPUT\_PULLUP | MUX\_MODE2) / i2c1\_scl.i2c1\_scl/ \*/

+ >;

+

+ edt\_ft5x06\_pins:edt\_ft5x06\_pins {

+ pinctrl-single,pins = <

+ 0x1AC 0x37

+ 0x154 0x17

+

+ >;

+ };

+ };

+/\*own enabled i2c end here.\*/

+

+

+

i2c2\_pins: pinmux\_i2c2\_pins {

pinctrl-single,pins = <

0x178 0x73 /\* (SLEWCTRL\_SLOW | PIN\_INPUT\_PULLUP | MUX\_MODE3) uart1\_ctsn.i2c2\_sda \*/

@@ -295,6 +316,43 @@

};

};

+

+

+

+

+

+

+/\*i2c1 enabled for slave ,selected with address=======>>\*/

+

+&i2c1 {

+ pinctrl-names = "default";

+ pinctrl-0 = <&i2c1\_pins>;

+

+ status = "okay";

+ clock-frequency = <400000>;

+

+ touch: edt-ft5x06@38 {

+ compatible = "edt,edt-ft5x06";

+ pinctrl-names = "default";

+ pinctrl-0 = <&edt\_ft5x06\_pins>;

+

+ reg = <0x38>;

+ interrupt-parent = <&gpio1>;

+ interrupts = <17 2>;

+/\* reset-gpios = <&gpio1 22 GPIO\_ACTIVE\_HIGH>;

+ wakeup-gpios = <&gpio1 >

+ touchscreen-size-x = <800>;

+ touchscreen-size-y = <480>; \*/

+ };

+};

+

+/\*end here xxxxxxxxxxx\*/

+

+

+

+

+

+

&i2c2 {

status = "okay";

pinctrl-names = "default";

--

1.9.1